

AACD 2023

XXXI Workshop on Advances in Analogue Circuit and Design Carinthia University of Applied Sciences (FH Karnten) Villach (Austria)

April, 12 - 14, 2023

https://www.mbtechnoservices.com/aacd23



Final Program

April. 12, 2023 8.00 Registration 8.45 Opening Cerimony Imaging Sensors — Chairman: Kofi Makinwa (TU Delft, The Netherlands) M. Pertijs, Y. Hopf, P. Guo (TU Delft, The Netherlands), "Pitch-Matched Integrated Circuits for Ultrasound Transducer Arrays" F. Widdershoven (NXP Semiconductors, The Netherlands), "Pixelated Capacitive Sensors for Embedded Multi-Sensing" 9.00 9.45 10.30 L. Gasparini (FBK, Italy), "Single-Photon Imagers for Time-Resolved Applications" F. Michel, C. Andreou, M. Rigo (ams OSRAM, Switzerland), "Photon Counting CT Detectors for Medical Imaging" 12.30 A. Faisal, F. Muhammad (Infineon Technologies, Austria), "High-Performance SiGe FMCW Radar Demonstrators at 120 GHz *14.00* and 240 GHz' 14.45 V. Jagtap and U. Pfeiffer (University of Wuppertal, Germany), "Broadband Terahertz Light-Field Camera for Single-Shot 3D Imaging" Coffee Break *15.30* 16.00 Panel Discussion 19.30 Welcome Cocktail Power Management — Chairman: Andrea Baschirotto (University of Milano-Bicocca, Italy) 9.00 G. Frattini (Analog Devices, Italy), "Integrated Isolated Power Conversion: Challenges and Opportunities" F. Pulvirenti (STMicroelectronics, Italy), "Design Solutions for Power Converters from System in Package to Monolithic GaN Technology" 10.30 11.00 Coffee Break F. Rezzi (Inventym, Italy), "Comparison of Standard Switching-Inductor Converter with Hybrid Topologies" F. Tavernier, T. Van Daele (KU Leuven, Belgium), "Design and Implementation of a HV DC-DC Converter for Large Conversion Steps in CMOS" 14.00 N. Zilio (Infineon, Austria), "Design, Control, and Implementation of Fully-Monolithic Hybrid Power Converters" J. Velner (Renesas, The Netherlands), "A SIMO Buck Converter for IoT Applications with < 1 µA Quiescent Current" 15.30 Coffee Break Panel Discussion 16.00 20.00 Gala Dinner April, 14, 2023 PLLs and Frequency Synthesizers — Chairman: Bram Nauta (University of Twente, The Netherlands) 9.00 B. Staszewski (University of Dublin, Ireland), "Beyond All-Digital PLL for RF and Millimeter-Wave Frequency Synthesis"
9.45 X. Gao (Zhejinang University, China), "Low Jitter Fractional-N PLL with Phase Detection and Quantization Noise Cancellation" 10.30 Coffee Break A. Mazzanti (University of Pavia, Italy), A. Franceschin (STMicroelectronics, Italy), D. Riccardi, (Infineon Technologies, Austria), "BiCMOS(Voltage-Controlled) Oscillators with Ultra-Low Phase Noise" 11.00

AACD 2023 Organization

11.45 F. Brandonisio (Maxlinear, Austria), "Digital PLLs for WiFi: State-of-the-Art and Trends"

D. Cherniak (Infineon Technologies, Austria), "Digitally-Intensive PLLs for mm-Wave FMCW Radars"

Scientific Committee

14.00

Andrea Baschirotto – University of Milan-Bicocca (Italy) Kofi Makinwa – TU Delft (The Netherlands) Bram Nauta – University of Twente (The Netherlands)

Organizing Committee

Christoph Sandner – Infineon Technologies (Austria) Andrea Baschirotto –University of Milano-Bicocca (Italy) Piero Malcovati – University of Pavia (Italy)

Local organizers

J. M. Velandia Torres (NXP Semiconductors, Austria), "Design and Implementation Challenges for PLLs in UWB Radios"





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